List of References


221


[MCNC93] MCNC Partitioning93 benchmark suite. E-mail benchmarks@mcnc.org for ftp access.


<table>
<thead>
<tr>
<th>Reference</th>
<th>Author(s)</th>
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Curriculum Vitae

Education
  Advisors: Gaetano Borriello and Carl Ebeling.

Research Interests
Multi-FPGA Systems, FPGA Architectures and CAD tools, Rapid-Prototyping, Asynchronous Circuit and VLSI Design, Parallel Processing and Parallel Programming Languages.

Awards
AT&T Bell Laboratories Graduate Fellowship
Berkeley Honors Society
National Merit Finalist

Employment
Northwestern University, Evanston, IL
  10/95 - 10/95  Assistant Professor.
University of Washington, Seattle, WA
  10/90 - 3/91  Teaching Assistant. Intro. to A.I. & Intro. to Operating Systems
University of California - Berkeley, Berkeley, CA
  1/90 - 5/90  Reader. Graded for CS170 - Intro. to Computer Science Theory
  1/87 - 5/89  Senior Engineering Aid. Participated in the design and development of the PICASSO user interface for the POSTGRES database project.
I.B.M. - T. J. Watson Research Center, Hawthorne, NY
  6/89 - 12/89  Temporary Employee - Co-op. Built a framework and toolkit for a set of X11-based Common Lisp programming tools. Wrote a data object inspector, and helped write a call graph/data-flow graph inspector and debugger. System was distributed commercially by Lucid, Inc. as XLT.
Bell Communications Research (Bellcore), Morristown, NJ

Publications
Journal Articles


**Patents and Book Chapters**


**Conference and Symposium Papers**


**Workshop Papers**


Technical Reports

